

IN THE CLAIMS:

1. (Cancelled).
2. (Currently Amended) The controller of claim 15, wherein said processor is integrated with said controller.
3. (Currently Amended) The controller of claim 15, wherein said processor provides processing capacity for use by said peripheral device in addition to processing of said one or more communication functions.
4. (Currently Amended) The controller of claim 15, wherein said at least one peripheral device employs said processor to perform each of said functions of said at least one peripheral device.
5. (Currently Amended) The A controller for high speed communications between a host computer and at least one peripheral device, comprising:
a processor for controlling communications on a bus using one or more communication functions, wherein said processor performs at least one function for said peripheral device in addition to said one or more communication functions of claim 1, wherein said high speed communications conform to a USB standard
6. (Cancelled).
7. (Cancelled).
8. (Cancelled).
9. (Currently Amended) The method of claim 811, wherein said first processor provides processing capacity for use by said peripheral device in addition to processing of said one or more communication functions.

10. (Currently Amended) The method of claim 8~~11~~, wherein said at least one peripheral device employs said first processor to perform each of said functions of said at least one peripheral device

11. (Currently Amended) The A method for controlling communications between a host computer and at least one peripheral device, comprising the step of:

executing one or more communication functions that control communications on a bus using a first processor, wherein said first processor also performs at least one function for said peripheral device in addition to said one or more communication functions of claim 8, wherein said high speed communications conform to a USB standard.

12. (Cancelled).

13. (Cancelled).

14. (Cancelled).

15. (Currently Amended) The integrated circuit of claim 14~~18~~, wherein said processor is integrated with said controller.

16. (Currently Amended) The integrated circuit of claim 14~~18~~, wherein said processor provides processing capacity for use by said peripheral device in addition to processing of said one or more communication functions.

17. (Currently Amended) The integrated circuit of claim 14~~18~~, wherein said at least one peripheral device employs said processor to perform each of said functions of said at least one peripheral device.

18. (Currently Amended) The An integrated circuit, comprising:
a controller for high speed communications between a host computer and at least one peripheral device, comprising:

a processor for controlling communications on a bus using one or more communication functions, wherein said processor performs at least one function for said peripheral device in addition to said one or more communication functions of claim 14, wherein said high speed communications conform to a USB standard.

19. (Cancelled).

20. (Cancelled).